

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes

Title: IN-SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER
INTERPOLY INSULATORS

Docket: 1303.027US2

Serial No.: 10/788,810

Filed: February 27, 2004

Due Date: December 13, 2004

Examiner: Igwe U Anya

Group Art Unit: 2825

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
- Amendment and Response Under 37 CFR 1.111 (14 Pages).
- Petition for Extension of Time (1 pg.)
- Authorization to charge Deposit Account 19-0743 in the amount of \$120.00 to cover the Extension of Time Fee.
- A Communication Concerning Related Applications (2 pgs.).
- A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.). Copies of cited US Patent Documents NOT enclosed, application filed after June 30, 2003.
- Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: 
Atty: Timothy B Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 13 day of December, 2004.

Name

Kacia Lee

Signature

Kacia Lee

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

S/N 10/788,810

PATENT

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Applicant: Leonard Forbes Examiner: Igwe U. Anya
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Filed: February 27, 2004 Docket: 1303.027US2
Title: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL
BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/929916	August 30, 2004	1303.035US2	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
10/819550	April 7, 2004	1303.019US3	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/931704	September 1, 2004	1303.014US2	FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/929986	August 30, 2004	1303.045US2	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
10/931711	September 1, 2004	1303.029US2	PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/788,810

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Title: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

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August 31, 2004 1303.020US2

PROGRAMMABLE ARRAY LOGIC OR
MEMORY DEVICES WITH
ASYMMETRICAL TUNNEL
BARRIERS

Respectfully submitted,

LEONARD FORBES

By Applicant's Representatives,

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Date

13 Dec '04

By


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Reg. No. 40,957

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Name

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Signature

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